

[BITLINE TWISTING STRUCTURE FOR MEMORY ARRAYS INCORPORATING REF- ERENCE WORDLINES]

Abstract

A bitline structure for a memory array includes a first pair of complementary bitlines and a second pair of complementary bitlines. Both the first and second pair of complementary bitlines have a twist at a location corresponding to about $\frac{1}{4}$ of the total length of the bitline structure. The second pair of complementary bitlines further have a twist at a location corresponding to about $\frac{1}{2}$ of the total length of the bitline structure, and both the first and second pair of complementary bitlines have a twist at a location corresponding to about $\frac{3}{4}$ of the total length of the bitline structure.